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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10083261	FILING DATE 02 25 2002	CLASS 349	SUBCLASS 41	GAU 2571	EXAMINER K2
**APPLICANTS: Chai Joong Kim, Hong Wan Shick, Kwon Dae-jin, Jung Hwan Wook, Kim Sang Gah, Chung Hye-ha					
**CONTINUING DATA VERIFIED:					
<p>BEST AVAILABLE COPY</p>					
** FOREIGN APPLICATIONS VERIFIED: REPUBLIC OF KOREA 2001 9675 02 26 2001 REPUBLIC OF KOREA 2001 60442 09 28 2001					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Title and Abstract Full Text and Abstract Full Text and Abstract with Claims				ATTORNEY DOCKET NO 8071 12 OPP 011059US	
TITLE : Thin film transistor array substrate using low dielectric insulating layer and method of fabricating the same					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims	
Amount Due	Date Paid	Print Claim for O.G.	
		DRAWING	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Application Examiner	
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